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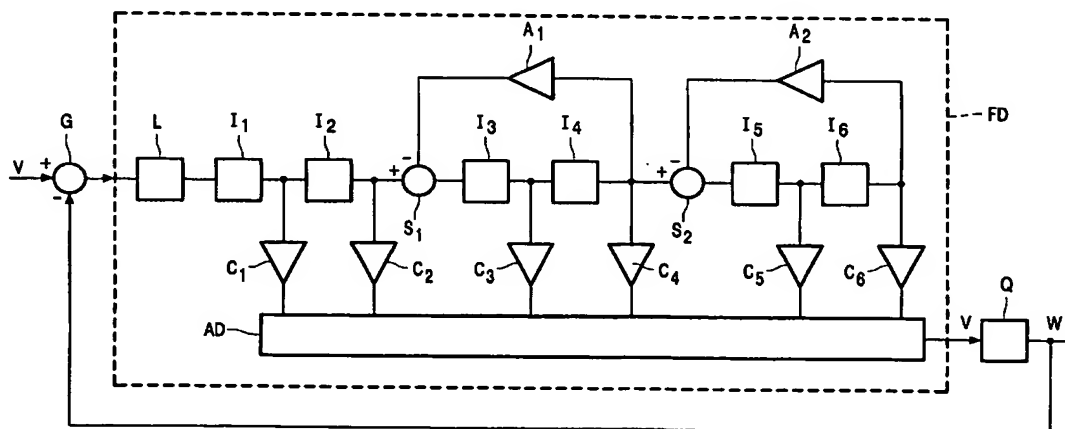
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A diagram showing a rectangular box with a dashed line extending from its top-left corner. The label A_2 is positioned below the dashed line.

A circuit diagram showing a 5V DC voltage source connected in series with a diode and a resistor. The diode is oriented with its anode towards the positive terminal of the 5V source. The resistor is connected in series with the diode. The circuit is shown as a single loop.



(S7) Abstract: A data converter comprises a discrete-time sigma delta modulator e.g. for driving a Class-D power amplifier. The low-pass filter of the sigma delta modulator is modified by adding a suitably positioned pole to lower the oscillation frequency (limit cycle) of the sigma delta modulator in order to obtain increased clustering of the pulses applied to the output of the data converter.

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